

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Burhnam et al.

Examiner: TOBERTGE, NICHOLAS J

S/N. No.: 10/604,905

Group Art Unit: 2823

Filed.: August 26, 2003

Docket. No.: BUR920020109US1

**Title: METHOD FOR FABRICATING A NITRIDED SILICON-OXIDE GATE  
DIELECTRIC**

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Honorable Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

**OFFICE ACTION RESPONSE**

This communication is in response to the Office Action mailed on October 31, 2006.